

Name : Er. Sita Devi
 Designation : Assistant Professor
 Department : Electrical Engineering
 Email I'd : sitajhjhriya100@gmail.com
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Areas of Interest :

Power Engineering, electrical machine, signal processing, instrumentation, digital, electric circuit analysis

Professional Background (Experience):

From	To	Designation	Organization
07-09-2016	Till Date	Assistant Professor	Ch. Devi Lal State Institute of Engineering and Technology, Panniwala Mota, (Sirsa) - 125077.

Honors and Awards:

Award	Institute/ Organization	Year

Educational Details:

Degree/ Diploma	Subject/ Stream	Institute	University	Maximum Marks	Obtained Marks	% of Marks	Year
B.Tech	Electrical Engineering	Ch. Devilal memorial govt. Engineering college, Panniwala mota	Ch. Devilal university, sirsa	6180	4660	75	2014
M.Tech	Electrical Engineering	Om institute of technology and management, hisar	Guru Jambheshwar university of science and technology	1800	1451	78	2016

Sponsored Research Projects:

Topic	Funding Agency	Year

Academic Contribution through organizing conferences/ seminars/ workshops/ symposia/ orientation /refresher courses:

Sr. No.	Coordinator/Convener/Secretary/Member of Organizing/Advisory Committee etc	Title of Short Term Course	Organizing Institute	Duration/Week	Sponsored By	Date

Memberships:

Teaching Engagements:

Title	Course Code	Class Name	Semester
Electrical circuit and network	EE-201-T	B.Tech. (EE)	3rd

Electrical workshop	PCC-EE-209-P	B.Tech. (EE)	3rd
Network analysis and synthesis	ESC-ECE-207-P	B.Tech. (ECE)	3rd
Power generation and control	EE-201-L	B.Tech. (EE)	3rd
Power system analysis and protection	EE-302-L	B. Tech. (EE)	6th
Major project	EE-420-P	B.Tech.(EE)	8th
High voltage Engineering	EE-416-T	B. Tech. (EE)	8th
Basics of electrical Engineering	EE-101-T	B. Tech(EE)	1st

Engagements other than Teaching:

- Member of Training and Placement cell and alumni cell of the Institute.
- Mentor of Final year of Department of Electrical Engineering.
- Member of anti ragging squad for girls hostel of the institute.
- Faculty coordinator member for the establishment of AICTE-IDEA Lab.

Participation in short term courses:

Sr. No.	Title of Short Term Course	Organizing Institute	Duration/ Week	Sponsored By	Date
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Books Authored:

Publications:

- Paper Published in International Journal

Sr. No.	Authors	Title of Paper	Year	Journal, Volume and No.	ISSN/ ISBN	Impact-Factor
1.	Sita Devi	Review of cuk converter for maximum power point tracking of photovoltaic system	2015	International journal (IJTR) having ISSN 2278-5787, Nov.-Dec. 2015 volume 5	ISSN 2278-5787	
2.	Sita Devi	Performance analysis of PI controller less incremental condition based MPPT technique for PV fed standalone variable resistive load	2016	International journal having ISSN 2278-5787 ,JULY-AUG. 2016 volume 5	ISSN:2278-5787	
3.	Sita devi	Maximum power tracking for photovoltaic power system	2021	International journal having ISSN 2347-4718, 10 June 2018, volume 5	ISSN: 2347-4718	
4.	Sita devi	Unbundling and competition in Indian electricity market	2021	International journal (IJTRE) having ISSN 2347-4718, 10 June 2018 volume 5	ISSN 2347-4718	

Paper Presented/Published in National/ International Conference/ Workshop/ Seminar:

Sr. No.	Authors Presented By	Title of Paper Presented	Title of the Conference/ Seminar etc and Organizer & Date (s) of the event	Whether International/ National/ State/ Regional/ University or College level	Whether published in the proceedings ISSN/ISBN	Remarks (API)
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Paper Review:

- Peer Reviewing of Papers of International Journal /Project Proposal/Evaluation Of Project Completion Report

Sr. No.	Authors	Title of Paper	Year	Journal, Volume and No.	ISSN/ ISBN	Remarks
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- Peer Reviewing of Papers National/ International Conference/ Workshop/ Seminar:

Sr. No.	Authors	Title of Paper / Article	Title of the Conference/ Seminar etc and Organizer	Year	ISSN/ ISBN	Remarks (API)
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Any Other Information:

Sr. No.	Event/ Information	Date
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Er. Sita Devi is working as Assistant Professor in Department (Electrical Engineering) at Ch. Devi Lal State Institute of Engineering and Technology Panniwala Mota (Sirsa) - Haryana, India- 125077. She is M.Tech (2016) in Electrical Engineering from Guru Jambheshwar university of science and technology, hisar, haryana, India. Her areas of interest are Power System, electrical machine, digital engineering, signal processing, instrumentation, electrical circuit analysis and network. She is having Research Papers publications in international/ national journals .She has 6year Teaching, Research and administrative Experience. She is working as Members of Various administrative and others Committee. She may be contacted through sitajhjhriya100@gmail.com or 8930342313.