

Ch. Devi Lal State Institute of Engineering & Technology, Panniwala Mota (Sirsā)
Electrical Engineering Department
Time Table Even Semester (2024-2025)

Branch: Electrical Engineering

Semester: 4th

Room no. 213

Total Load: 44 Hours

DAY	LECTURE		3	4	12:40-01:20	5		7	8
	1	2				01:20-02:15	02:15-03:10		
MONDAY	POWER ELECTRONICS Puneet Chawla 212	POWER SYSTEM-I Shivani Jindal	FIELD & WAVES Puneet Chawla	ESSENCE OF INDIAN TRADITION & KNOWLEDGE Dr Meena Kumari	L	ELECTRICAL MACHINES-II LAB G1 Sita Devi 114			
	POWER ELECTRONICS Puneet Chawla 212	POWER SYSTEM-I Shivani Jindal	FIELD & WAVES Puneet Chawla	ESSENCE OF INDIAN TRADITION & KNOWLEDGE Dr Meena Kumari		FIELD & WAVES (TD) G2 Sanjay	POWER ELECTRONICS LAB G2 Raman Kamboj 201		
TUESDAY	POWER ELECTRONICS Puneet Chawla 212	POWER SYSTEM-I Shivani Jindal	ELECTRICAL MACHINES-II Sita Devi	ESSENCE OF INDIAN TRADITION & KNOWLEDGE Dr Meena Kumari	U	ELECTRICAL MACHINES-II (TD) G1 Sita Devi 202	HUMAN VALUES & PERSONALITY DEVELOPMENT Deepak Kumar	POWER ELECTRONICS LAB G1 Raman Kamboj 201	POWER SYSTEM-LAB G2 Shivani Jindal 202
	POWER ELECTRONICS Puneet Chawla 212	POWER SYSTEM-I Shivani Jindal	ELECTRICAL MACHINES-II Sita Devi	ESSENCE OF INDIAN TRADITION & KNOWLEDGE Dr Meena Kumari		POWER SYSTEM-LAB G1 Shivani Jindal 202			
WEDNESDAY	POWER ELECTRONICS Puneet Chawla 212	SIGNAL & SYSTEM GF (EE)	ELECTRICAL MACHINES-II Sita Devi	SIGNAL & SYSTEM GF (EE)	N	ELECTRICAL MACHINES-II (TD) G2 Sita Devi 202	HUMAN VALUES & PERSONALITY DEVELOPMENT Deepak Kumar	POWER ELECTRONICS (TD) G2 GF (EE)	SIGNAL & SYSTEM (TD) G2 GF (EE)
	POWER ELECTRONICS Puneet Chawla 212	SIGNAL & SYSTEM GF (EE)	ELECTRICAL MACHINES-II Sita Devi	SIGNAL & SYSTEM GF (EE)		POWER SYSTEM-LAB G2 Sita Devi 114			
THURSDAY	FIELD & WAVES Puneet Chawla	POWER SYSTEM-I Shivani Jindal	ELECTRICAL MACHINES-II Sita Devi	SIGNAL & SYSTEM GF (EE)	C	FIELD & WAVES (TD) G1 Sanjay	POWER ELECTRONICS (TD) G1 GF (EE)	SIGNAL & SYSTEM (TD) G1 GF (EE)	
	FIELD & WAVES Puneet Chawla	POWER SYSTEM-I Shivani Jindal	ELECTRICAL MACHINES-II Sita Devi	SIGNAL & SYSTEM GF (EE)		POWER SYSTEM-LAB G2 Sita Devi 114			
FRIDAY	FIELD & WAVES Puneet Chawla	POWER SYSTEM-I Shivani Jindal	SIGNAL & SYSTEM GF (EE)		H	FIELD & WAVES (TD) G1 Sanjay	POWER ELECTRONICS (TD) G1 GF (EE)	SIGNAL & SYSTEM (TD) G1 GF (EE)	
	FIELD & WAVES Puneet Chawla	POWER SYSTEM-I Shivani Jindal	SIGNAL & SYSTEM GF (EE)			POWER SYSTEM-LAB G2 Sita Devi 114			

(Signature)
 Head (Electrical Engg.)

CDL State Institute of Engg. & Tech.
 Panniwala Mota (Sirsā) (Engg.)

CDL State Institute of Engg. & Tech.

Engg. & Tech.

Panniwala Mota (Sirsā)



Ch. Devi Lal State Institute of Engineering & Technology, Panniwala Mota (Sirsa)

Electrical Engineering Department
Time Table Even Semester (2024-2025)

Branch: Electrical Engineering

Semester: 6th

Room no. 221

Total Load: 35 Hours

LECTURE	1	2	3	4	5	6	7	8	
DAY	09:00-09:55	09:55-10:50	10:50-11:45	11:45-12:40	12:40-01:20	01:20-02:15	02:15-03:10	03:10-04:05	04:05-05:00
MONDAY		POWER SYSTEM-II Ruby Sathhala	CONTROL SYSTEM-II GF (EE)	FUNDAMENTALS OF INFORMATION SECURITY Ankit		ECONOMICS FOR ENGINEERS Anu Bala			
TUESDAY		POWER SYSTEM-II Ruby Sathhala	CONTROL SYSTEM-II GF (EE)	ELECTRICAL MEASUREMENT & INSTRUMENTATION Shivani Jindal		ECONOMICS FOR ENGINEERS Anu Bala			
WEDNESDAY		ELECTRICAL MEASUREMENT & INSTRUMENTATION Shivani Jindal	CONTROL SYSTEM-II (T) G1 GF (EE)	RENEWABLE ENERGY RESOURCES Raman Kanhoj		FUNDAMENTALS OF INFORMATION SECURITY Ankit			
THURSDAY		ELECTRICAL MEASUREMENT & INSTRUMENTATION Shivani Jindal	CONTROL SYSTEM-II (T) G2 GF (EE)	RENEWABLE ENERGY RESOURCES Raman Kanhoj		POWER SYSTEM-II LAB G1 Ruby Sathhala 202			
FRIDAY	CONTROL SYSTEM-II GF (EE)	POWER SYSTEM-II Ruby Sathhala	FUNDAMENTALS OF INFORMATION SECURITY Ankit	RENEWABLE ENERGY RESOURCES Raman Kanhoj	H	ELECTRICAL MEASUREMENT & INSTRUMENTATION LAB G1 Shivani Jindal 201	CONTROL SYSTEM-II LAB G1 Shivani Jindal 201		
						ELECTRICAL MEASUREMENT & INSTRUMENTATION LAB G2 Shivani Jindal 201	CONTROL SYSTEM-II LAB G2 Shivani Jindal 201		

(Signature)
24/1/25

Head (Electrical Engrg.)

CDL State Institute of Engg. & Tech.

Panniwala Mota (Sirsa)

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Engg. & Tech

Panniwala Mota (Sirsa)



Ch. Devi Lal State Institute of Engineering & Technology, Panniwala Mota (Sirsa)

Electrical Engineering Department

Time Table Even Semester (2024-2025)

Branch: Electrical Engineering

Semester: 8th

Room no. 222

Total Load: 39 Hours

LECTURE	1	2	3	4	5	6	7	8	
DAY	09:00-09:55	09:55-10:50	10:50-11:45	11:45-12:40	12:40-01:20	01:20-02:15	02:15-03:10	03:10-04:05	04:05-05:00
MONDAY			EXTRA HIGH VOLTAGE AC & DC TRANSMISSION Raman Kamboj	DISTRIBUTED GENERATION Ruby Sathiala	L	COMPUTER METHODS IN POWER SYSTEM (D) G1 Puneet Chawla 201	MAJOR PROJECT G1 Ruby Sathiala		MAJOR PROJECT G2 GF (EE) 228
			COMPUTER METHODS IN POWER SYSTEM LAB G1 Raman Kamboj 102	EXTRA HIGH VOLTAGE AC & DC TRANSMISSION Raman Kamboj		COMPUTER METHODS IN POWER SYSTEM (D) G2 Puneet Chawla 201	MAJOR PROJECT G1 Ruby Sathiala		MAJOR PROJECT G2 GF (EE) 228
TUESDAY			COMPUTER METHODS IN POWER SYSTEM Puneet Chawla	EXTRA HIGH VOLTAGE AC & DC TRANSMISSION Raman Kamboj	U	MAJOR PROJECT G1 Puneet Chawla		MAJOR PROJECT G2 GF (EE) 228	
WEDNESDAY	COMPUTER METHODS IN POWER SYSTEM LAB G2 Raman Kamboj 102		COMPUTER METHODS IN POWER SYSTEM Puneet Chawla	DISTRIBUTED GENERATION Ruby Sathiala		N	MAJOR PROJECT G1 Puneet Chawla		MAJOR PROJECT G2 Raman Kamboj 228
THURSDAY			COMPUTER METHODS IN POWER SYSTEM Puneet Chawla	DISTRIBUTED GENERATION Ruby Sathiala	C		MAJOR PROJECT G1 Puneet Chawla		MAJOR PROJECT G2 Raman Kamboj 228
	EXTRA HIGH VOLTAGE AC & DC TRANSMISSION Raman Kamboj		COMPUTER METHODS IN POWER SYSTEM Puneet Chawla	DISTRIBUTED GENERATION Ruby Sathiala		MAJOR PROJECT G1 Puneet Chawla		MAJOR PROJECT G2 Raman Kamboj 228	
FRIDAY					H				

(Signature)
Head (Electrical Engg.)

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